

S/N 10/643,585

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven L. Scott

Examiner: Sheng J. Tsai

Serial No.: 10/643,585

Group Art Unit: 2186

Filed: August 18, 2003

Docket: 1376.700US1

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY
MULTIPROCESSOR COMPUTER



COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/643,577	August 18, 2003	1376.711US1	SYSTEM AND METHOD FOR PROCESSING MEMORY INSTRUCTIONS
10/643,587	August 18, 2003	1376.717US1	SCHEDULING SYNCHRONIZATION OF PROGRAMS RUNNING AS STREAMS ON MULTIPLE PROCESSORS
10/643,769	August 18, 2003	1376.718US1	SCHEDULING SYNCHRONIZATION OF PROGRAMS RUNNING AS STREAMS ON MULTIPLE PROCESSORS
10/643,767	August 18, 2003	1376.719US1	SIMULATING ACCESS TO A SHARED RESOURCE WITHOUT CONSUMING CPU CYCLES
10/643,738	August 18, 2003	1376.726US1	SYSTEM AND METHOD FOR PERFORMING TRANSLATION LOOKASIDE BUFFER OPERATIONS
10/643,574	August 18, 2003	1376.730US1	INDIRECTLY ADDRESSED VECTOR LOAD-OPERATE-STORE METHOD AND APPARATUS
10/643,727	August 18, 2003	1376.731US1	METHOD AND APPARATUS FOR INDIRECTLY ADDRESSED VECTOR LOAD-ADD-STORE ACROSS MULTI-PROCESSORS

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1376.731US2

METHOD AND APPARATUS FOR
INDIRECTLY ADDRESSED VECTOR
LOAD-ADD-STORE ACROSS MULTI-
PROCESSORS

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

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Date March 10, 2008 By Thomas F. Brennan
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 10 day of March, 2008.

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